

J102 U.S. PTO
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01/25/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10056631	01/25/2002	710	110	2110 2184	Deary

**APPLICANTS: Worrell Frank;

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/271,848 02/27/2001

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	00-315 1496.00056	
Verified and Acknowledged Examiners's initials			
TITLE : On-chip bus			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED

Assistant Examiner

CLAIMS ALLOWED

Total Claims Print Claim for O.G.

DRAWING

Sheets Drawg. Figs. Drawg. Print Fig.